

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph [0013] as follows:

[0013] In a variation of this embodiment, the system converts the internal frequency signal and the external frequency signal into corresponding current signals, and then ~~compare~~ compares the two current signals to generate a difference current signal. The difference current signal is then coupled to an integrating capacitor to produce an integrated voltage signal. The system also applies an offset current source to the integrating capacitor to compensate for transistor leakages, parasitics, and/or nonlinearities. The system further includes an amplifier which is coupled to the integrating capacitor, wherein the input to the amplifier is the integrated voltage signal and the output of the amplifier is the control signal.